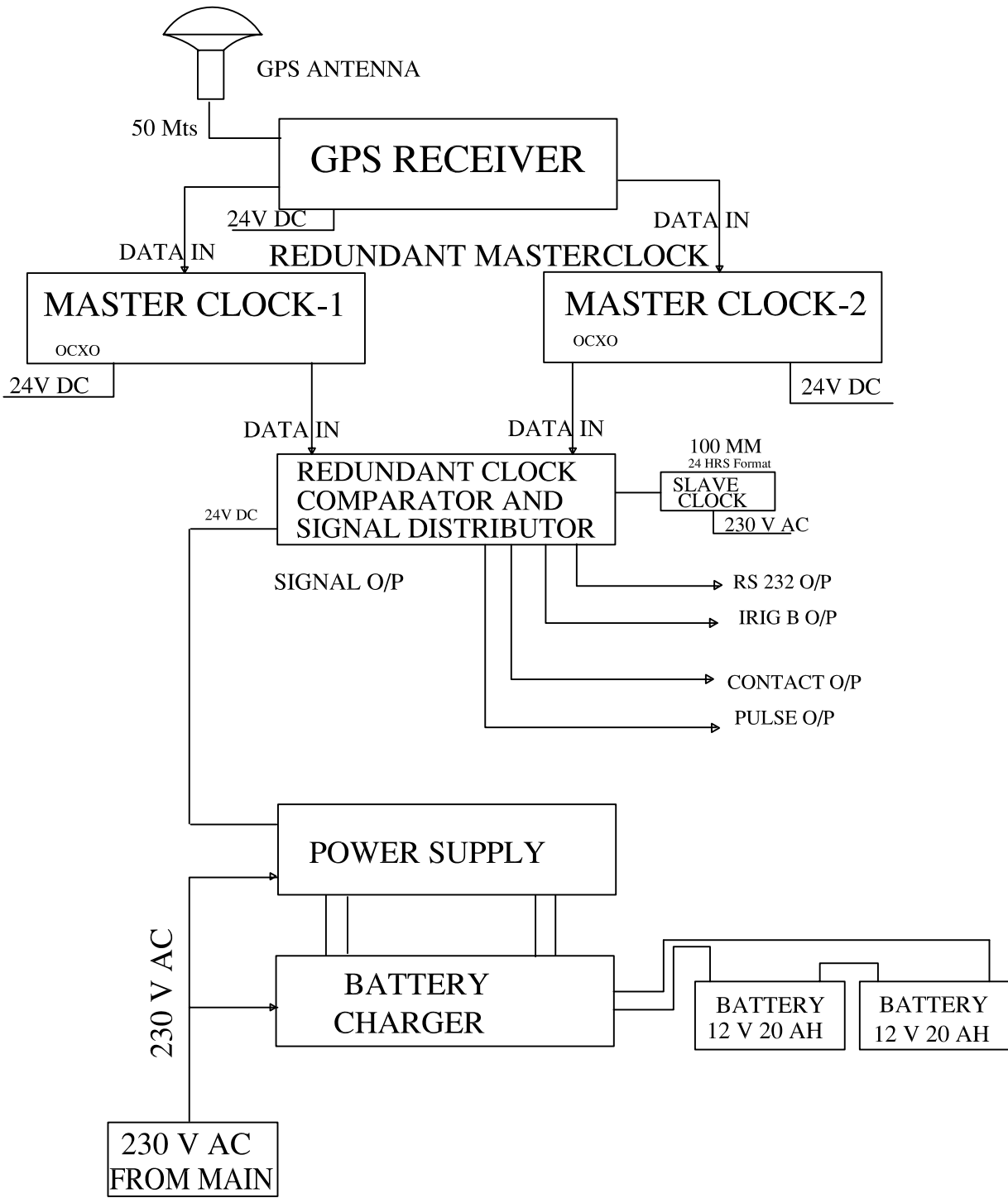


REDUNDANT MASTERCLOCK



DRAWING NO.:

2003 / RGN / J6 / 212

CHENNAI -96

SERTEL ELECTRONICS PVT. LIMITED.,

SCHEME DIAGRAM:
GPS AND REDUNDANT MASTER CLOCK SYSTEM CONFIGURATION

TYPICAL DRAWING

DRN. BY: GS	DATE: 28-10-03
CHKD. BY: GS	
APPD. BY: RGN	

DRN	CHKD	APPD	DATE

REV	REVISION DETAILS	DRN	CHKD	APPD	DATE