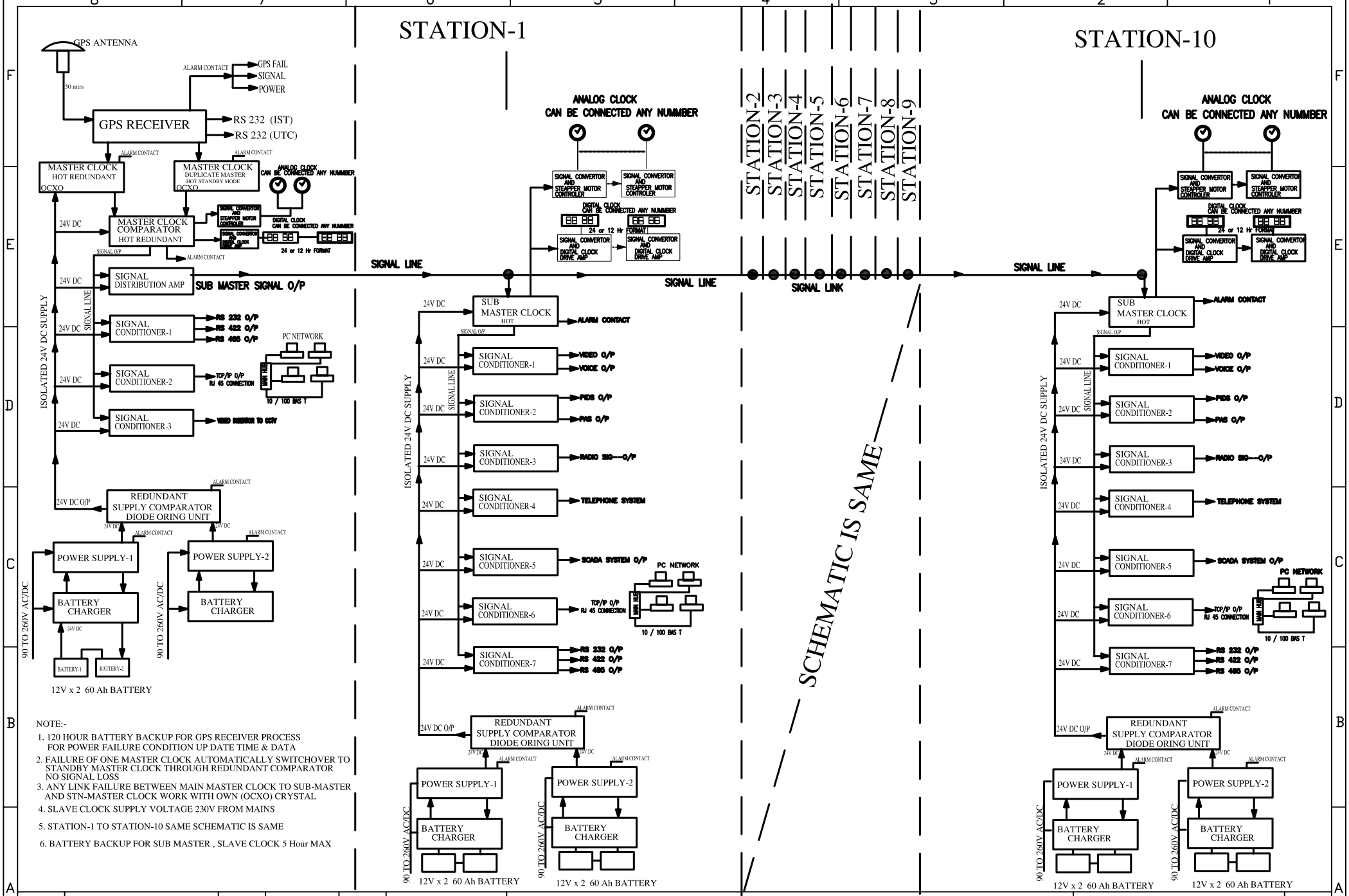

NOTE:-

1. 120 HOUR BATTERY BACKUP FOR GPS RECEIVER PROCESS FOR POWER FAILURE CONDITION UP DATE TIME & DATA
2. FAILURE OF ONE MASTER CLOCK AUTOMATICALLY SWITCHOVER TO STANDBY MASTER CLOCK THROUGH REDUNDANT COMPARATOR NO SIGNAL LOSS
3. ANY LINK FAILURE BETWEEN MAIN MASTER CLOCK TO SUB-MASTER AND STN-MASTER CLOCK WORK WITH OWN (OCXO) CRYSTAL
4. SLAVE CLOCK SUPPLY VOLTAGE 230V FROM MAINS
5. STATION-1 TO STATION-10 SAME SCHEMATIC IS SAME
6. BATTERY BACKUP FOR SUB MASTER , SLAVE CLOCK 5 Hour MAX

REV	REVISION DETAILS	DRN	CHKD	APPD	DATE

DRN. BY: GS
CHKD. BY: GS
APPD. BY: RGN
DATE: 22-06-06

SERTEL ELECTRONICS PVT. LIMITED., CHENNAI -96

SCHEME DIAGRAM:
GPS/ MASTER CLOCK AND SUB MASTER CLOCK SYSTEM GA DRAWING

TYPICAL DRAWING

DRAWING NO. :
2006/RGN/06D/6065 Q

SHEET 1 OF 1

REV